Theoretical and Experimental Investigation of the Fast- and Slow-Scale Instabilities of a DC–DC Converter

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Abstract—We use an exact formulation based on nonlinear maps to investigate both the fast-scale and slow-scale instabilities of a voltage-mode buck converter operating in the continuous conduction mode and its interaction with a filter. Comparing the results of the exact model with those of the averaged model shows the shortcomings of the latter in predicting fast-scale instabilities. We show the impact of parasitics on the onset of chaos using a high-frequency model. The experimentally validated theoretical results of this paper provide an improved understanding of the dynamics of the converter beyond the linear regime and this may lead to less conservative control design and newer applications.

Index Terms—Bifurcation, converters, differential inclusion, discontinuous systems.

I. INTRODUCTION

POWER ELECTRONIC converters are nonlinear dynamical systems. The nonlinearities arise primarily due to switching, power devices, and passive components, such as transformers, inductors, and parasitics. Historically, there have been four major approaches to the modeling and analysis of the switching nonlinearity in dc–dc converters. The most widely used approach is a small-signal analysis based on state-space averaging or circuit averaging using the pulse-width modulation (PWM) switch model [1]–[4]. This approach fails to predict the fast-scale dynamics and can capture only the slow-scale dynamics. The second approach is based on the sampled-data modeling technique [5]–[8]. It is an improvement over the averaged modeling technique. It takes into account the sampling effect due to switching and can predict the boundary of the period-one instability. It is currently, however, limited to current-mode converters and is difficult to use for analyzing chaotic dynamics. The third approach describes the switched-mode power supply using a typical continuous time model of the form \( \dot{\vec{x}} = \vec{f}(\vec{x}, u, t) \), where the right-hand side is discontinuous due to abrupt changes in the control \( u \). This class of models is one of the hardest to study [9]–[13]. Besides, the existence of solutions for this class of systems is not always well-defined. On the other hand, a discrete-time formulation of the switched-mode operation does not involve discontinuities due to control action and results in smooth functions that describe the system. The last approach, which is known as the discrete modeling technique, was first used by Prajoux et al. [14] for power converter modeling. Later, it was used by Wood [15] and Deane and Hamill [16] to investigate the regions of the fast-scale instability in buck converters operating in the continuous-conduction mode (CCM). Since then some exciting results have been published, which extend this investigation to a few other dc–dc converters operating in the CCM and the discontinuous conduction mode (DCM) [17]–[26].

Most of the earlier work in this field dealt with idealized circuits and demonstrated some of the nonlinear phenomenon in such bare-bone systems. In this paper, we treat the converter as a power-electronic system and not just as a circuit. Using an exact formulation based on nonlinear maps [14]–[16], we develop a systematic method to model dc–dc converters operating with static or dynamic feedback control. We use this methodology to investigate the fast-scale instabilities of a high-frequency voltage-mode buck converter that employs dynamic-feedback control for voltage regulation and operates in the CCM. The basic idea can, however, be easily extended to other classes of dc–dc converters, including parallel dc–dc converters [26], and even single-phase power-factor-correction circuits [13]. We validate experimentally the theoretical results for the voltage-mode buck converter. We also extend this analysis to an integrated system involving a voltage-mode buck converter and a second-order input filter at its front end. For both cases, we compare the results obtained with the exact formulation with those obtained using state-space averaged models and point out the shortcomings of averaged models in predicting fast-scale instabilities in systems that are closer to converters used in practice. Hamill [27], pointed out the shortcomings of averaged models in predicting fast-scale instabilities in an idealized circuit representing a buck converter. The approach in this paper is systematic and can be used to investigate slow- and fast-scale instabilities in any other class of dc–dc converters [26]. It can also be applied to systems with static- or dynamic-feedback control, or multiloop control with minimal changes. For example, in [26], we have extended the methodology to parallel dc–dc converters. Unlike [27], the analysis in this paper includes the effect of the parasitics in the nominal model and extends the methodology to analyze the stability of an integrated converter.
Currently, most of the commercial dc–dc converters operate at 100 kHz or above. At such high frequencies the effects of parasitic elements can not be ignored as has been done in the past by most investigators. Therefore, in this paper we also demonstrate the impact of very high-frequency dynamics, due to parasitics and device nonlinearities, on the onset of chaos by developing a high-frequency model. The parasitic parameters for this model are obtained using a finite-element analysis package based on the actual printed-circuit board (PCB) of the experimental converter.

The analyses in this paper are based on systems that are close to dc–dc converters used in practice and hence, the results should be of interest to practicing engineers. The present results indicate that nonlinear analyzes of power converters lead to a better understanding of their dynamics. With these analyzes, one can clearly demarcate the boundaries of instabilities (without resorting to time consuming numerical simulations) and demonstrate the fast-scale and slow-scale instabilities. This may lead to converters that have better design and performance.

II. MODELING AND ANALYSIS

Initially, we assume that the nonlinearities due to the power device and parasitics are negligible. Then, we demonstrate their effect through a high-frequency model. The converter is clocked at a rate equal to the switching frequency. Moreover, the controller is designed in such a way that, once a change of state is latched, it can be reset only by the next clock. This effectively eliminates the possibility of multiple pulses. We analyze the closed-loop buck converter (without the filter) first and then extend the analysis to the integrated system.

Buck converter operating under the CCM and duty-ratio control is a piecewise-smooth system. The multitopological system, shown in Fig. 1, is in the on-state (for duration $T_1$) when $S_1$ is closed and in the off-state (for duration $T_2$) when $S_2$ is open. If we represent the two states, the inductor current $v(t)$ and the output capacitor voltage $v_{oc}(t)$ of the open-loop converter, by $X(t)$, then we write the system of equations governing the two states as

\[
\begin{align*}
\frac{dX(t)}{dt} &= A_1^o X(t) + B_1^o u \\
v_{oc}(t) &= C_1^o X(t)
\end{align*}
\]

for $0 \leq t < T_1$ and

\[
\begin{align*}
\frac{dX(t)}{dt} &= A_2^o X(t) + B_2^o u \\
v_{oc}(t) &= C_2^o X(t)
\end{align*}
\]

for $T_1 \leq t < T_2$, where

\[T = T_1 + T_2.
\]

In (1) and (2), $v_{oc}(t)$ is the sum of $V(t)$ and the voltage drop across $r_C$. The matrices $A_1^o, A_2^o, B_1^o, B_2^o, C_1^o, \text{ and } C_2^o$ in (1) and (2) are given in Appendix I.

Using (1) and (2), we derive the open-loop state-space averaged model by taking the average of the states in the on- and off-intervals. This yields the following large-signal time-varying continuous system

\[
\begin{align*}
\frac{dX(t)}{dt} &= (A_1^o d + A_2^o d) X(t) + (B_1^o d + B_2^o d) u \\
v_{oc}(t) &= (C_1^o d + C_2^o d) X(t)
\end{align*}
\]

where the duty ratio $d = T_1/T$.

Next we derive the exact solution of the open-loop system by stacking the consecutive solutions of (1) and (2) over
a switching period. The resulting discrete-time difference equation can be written in state-space form as

\[ X_{k+1} = f_1^b(X_k, d_k, u) = \Phi(d_k)X_k + \Gamma(d_k)u \]  
\[ V_{k+1} = f_2^b(X_k, d_k, u) = C_2^b X_{k+1} \]  

where

\[ \Phi(d_k) = \Phi_2(1 - d_k)\Phi_1(d_k), \quad \Phi_1(\tau) = e^{A_1^b \tau} \]  
\[ \Gamma(d_k) = \Phi_2(1 - d_k) \int_0^{d_k T} \Phi_1(\tau)B_1^b d\tau \]  

(6)

Using

\[ \int_0^t e^{A_1^b \tau} B_1^b d\tau = \left[ e^{A_1^b \tau} - I \right] (A_1^b)^{-1} B_1^b \]  

(7)

and (6) and (7), we simplify the expression for \( X_{k+1} \) in (5a) to

\[ X_{k+1} = f_1^b(X_k, d_k, u) = e^{A_1^b (1 - d_k) T} e^{A_1^b d_k T} X_k + e^{A_1^b (1 - d_k) T} \left[ e^{A_1^b d_k T} - I \right] (A_1^b)^{-1} B_1^b u \]  

+ \[ e^{A_1^b (1 - d_k) T} \left[ e^{A_1^b d_k T} - I \right] (A_1^b)^{-1} B_2^b u. \]  

(8)

To derive a model for the closed-loop system, we assume that the \( m \)th-order, linear, time-invariant error amplifier (shown in Fig. 1) can be modeled as

\[ \xi(t) = A_c \xi(t) + B_c u + B_{rec} V_{dref} \]  
\[ V_{e}(t) = H_c \xi(t) \]  

where

\[ \xi(t) \]  
\[ m \times 1 \]  
\[ \text{state vector representing the states of the controller;} \]
\[ A_c \]  
\[ \text{constant matrix;} \]
\[ B_c \text{ and } B_{rec} \]  
\[ m \times 1 \text{ column vectors;} \]
\[ H_c \]  
\[ 1 \times m \text{ row vector;} \]
\[ V_{dref} \]  
\[ \text{reference voltage;} \]
\[ V_{e}(t) \]  
\[ \text{output of the error amplifier.} \]

Using (1), (2), and (10), we obtain the following equations for the closed-loop converter system

\[ \frac{d\Psi(t)}{dt} = A_1 \Psi(t) + B_{1u} + B_{1v} V_{dref} \]  
\[ V_k(t) = C_1 \Psi(t) \]  
\[ V_{e}(t) = P_1 \Psi(t) \]  

(10a)

(10b)

(10c)

for \( 0 \leq t < T_1 \) and

\[ \frac{d\Psi(t)}{dt} = A_2 \Psi(t) + B_{2u} + B_{2v} V_{dref} \]  
\[ V_k(t) = C_2 \Psi(t) \]  
\[ V_{e}(t) = P_2 \Psi(t) \]  

(11a)

(11b)

(11c)

for \( T_1 \leq t < T \). The matrices \( A_1, A_2, B_1, B_2, B_{1u}, B_{1v}, C_1, \) and \( C_2 \) in (11) and (12) are given in Appendix II. Now \( \Psi(t) \) represents the combined states of the controller and the power stage.

Using (11) and (12), we obtain the following state-space averaged model for the closed-loop system

\[ \frac{d\Psi(t)}{dt} = [A_1 d + A_2 d_{\text{ref}}] \Psi(t) + [B_1 d + B_{2u}] u \]  
\[ + [B_{1v} d + B_{2v}] V_{dref} \]  
\[ V_k(t) = (C_1 d + C_2 d_{\text{ref}}) \Psi(t). \]  

(13a)

(13b)

Similarly, we can write an exact discrete model for the closed-loop system in the form

\[ \Psi_{k+1} = f_1(\Psi_k, d_k, u) \]

\[ = e^{A_2 (1 - d_k) T} e^{A_1 d_k T} \Psi_k \]

\[ + \left[ e^{A_2 (1 - d_k) T} (e^{A_1 d_k T} - I) (A_1)^{-1} B_1 \right] u \]

\[ + \left[ e^{A_2 (1 - d_k) T} (e^{A_1 d_k T} - I) (A_2)^{-1} B_2 \right] u \]

\[ V_{dref} = f_2(\Psi_k, d_k, u) = C_2 \Psi_{k+1}. \]  

(14a)

(14b)

The auxiliary equation for the switching condition of the closed-loop feedback system is

\[ \sigma(\Psi_k, d_k; u) = \varphi \cdot \left[ e^{A_1 d_k T} \Psi_k + (e^{A_1 d_k T} - I) (A_1)^{-1} B_1 \right] \]

\[ \times (B_1 u + B_{1v} V_{dref}) - V_{\text{ramp}} d_k \]

\[ = 0. \]  

(15)

In (15), the term \( \varphi \) represents the feedback controller and \( V_{\text{ramp}} \) is the magnitude of the ramp shown in Fig. 1. For example, for a lag-lead controller with an integrator

\[ \varphi = \omega_m (0 \quad 0 \quad \omega_{12} \quad (\omega_{12} + \omega_{23} \quad 1). \]

The transfer function of the controller is

\[ G_c(s) = \frac{\omega_I (s + \omega_{12}) (s + \omega_{23})}{s (s + \omega_{12}) (s + \omega_{23})} \]

\[ = \frac{\omega_m \left( \frac{s}{\omega_{12}} + 1 \right) \left( \frac{s}{\omega_{23}} + 1 \right)}{s \left( \frac{s}{\omega_{12}} + 1 \right) \left( \frac{s}{\omega_{23}} + 1 \right)} \]  

(16)

where \( \omega_I \) is the integrator gain and \( \omega_{12}, \omega_{23}, \omega_{12}, \) and \( \omega_{23} \) are the zeros and poles of the controller.

For the filter shown in Fig. 1, the overall system analysis is done by augmenting the state-space representation of the voltage-mode converter with additional states of the filter, which forms its front stage. In this paper, we have chosen two second-order input filters having the following output impedance transfer function:

\[ Z_{\text{ref}}(s) = \frac{s^2 (L_f C_f R_f) + s (C_f R_f 1 + R_f 1) + R_f 1}{s^2 (L_f C_f) + s C_f (R_f 1 + R_f 2) + 1}. \]  

(17)

### III. Period-One Responses and Their Stability

The fixed points \( \Psi_k \) of (14) correspond to period-one limit cycles of the closed-loop regulator. They can be obtained by using the constraint \( \Psi_{k+1} = \Psi_k = \Psi_s \). Letting \( U = u \) and \( D = d_k \)
Substituting (20) into (14) and (15), expanding the results in Taylor series, and keeping first-order terms, we obtain

\[ \frac{\partial \bar{\Psi}}{\partial \bar{u}} \approx \frac{\partial \bar{\Psi}}{\partial \bar{u}} \frac{\partial \bar{u}}{\partial \bar{u}} - \frac{\partial \bar{\Psi}}{\partial \bar{u}} \frac{\partial \bar{u}}{\partial \bar{u}} \]

\[ \hat{U}_{k+1} = H_1 \hat{X}_k + H_2 \hat{U}_k \]

\[ \hat{V}_{k+1} = H_1 \hat{X}_k + H_2 \hat{U}_k \]

The stability of a given fixed point can be ascertained by the eigenvalues (Floquet multipliers) of \( H_1 \) [28], [29]. For asymptotic stability, all of the Floquet multipliers must be within the unit circle. As a control parameter, such as the input voltage, is varied, we found that the fixed point loses stability by one of two scenarios. In the first scenario, a Floquet multiplier exits the unit circle in the complex plane through \( -1 \). The post-instability response is a period-two limit cycle and the bifurcation is a flip or a period-doubling bifurcation [28], [29]. The bifurcation may be supercritical or subcritical, depending on whether the created period-two fixed point is stable or unstable. For supercritical bifurcations, the created period-two fixed point coexists with the unstable period-one fixed point. On the other hand, for subcritical bifurcations, the created period-two fixed point coexists with the stable period-one fixed point. In the second scenario, two complex conjugate Floquet multipliers exit the unit circle away from the real axis. The post-instability response is two-period quasiperiodic and the bifurcation is a Hopf bifurcation [28], [29]. The normal form of the Hopf bifurcation can be used to determine whether it is subcritical or supercritical. Alternatively, for supercritical bifurcations, the created quasiperiodic response coexists with the unstable period-one response; whereas for subcritical bifurcations, the created quasiperiodic response coexists with the stable period-one response.

**IV. Period-Two Fixed Points and Their Stability**

To investigate the behavior of the period-doubled response, we construct a second-order map by imposing the constraint \( \Psi_{k+2} = \Psi_k = \Psi_{2k} \). Using a methodology similar to that used in the period-one case, we construct the second-order map (27) and (28), shown at the bottom of the next page, where \( D_1, D_2, \bar{\Psi}_{1s}, \) and \( \bar{\Psi}_{2s} \) are the duty ratios and states corresponding to the period-two fixed points. The switching conditions are

\[ \sigma_1(\Psi_{1s}, D_1, U) = \varphi \cdot \left( c_{A_1} D_1 T \Psi_{1s} + (c_{A_1} D_1 T - I)(A_1) \right)^{-1} \times (B_1 U + B_{1r} V_{dref}) \]

\[ \sigma_2(\Psi_{2s}, D_2, U) = \varphi \cdot \left( c_{A_1} D_1 T \Psi_{2s} + (c_{A_1} D_1 T - I)(A_1) \right)^{-1} \times (B_1 U + B_{1r} V_{dref}) \]

The procedure for determining \( D_1, D_2, \bar{\Psi}_{1s}, \) and \( \bar{\Psi}_{2s} \) is the same as that used for the period-one fixed point except that we
need two initial guesses for the duty ratios. Guessing \(D_1\) and \(D_2\), we determine \(\Psi_{1s}\) and \(\Psi_{2s}\) using (27) and (28) and then correct \(D_1\) and \(D_2\) using (29) and (30).

The stability of the period-two fixed points can be determined as before by perturbing them and forming the linearized variational equations

\[
\dot{\Psi}_{k+2} = H_5 \dot{\Psi}_k + H_6 \dot{u}, \tag{31}
\]
\[
\dot{V}_{a0,k+2} = H_5 \dot{V}_{a0,k} + H_6 \dot{u}, \tag{32}
\]

Again the stability of the period-two orbit can be determined by calculating the Floquet multipliers of \(H_5\), which can be shown to be

\[
H_5 = \left[ \frac{\partial f_1}{\partial V_2} - \frac{\partial f_1}{\partial d_2} \left( \frac{\partial \sigma}{\partial d_2} \right)^{-1} \frac{\partial \sigma}{\partial V_2} \right] \times \left[ \frac{\partial f_1}{\partial V_1} - \frac{\partial f_1}{\partial d_1} \left( \frac{\partial \sigma}{\partial d_1} \right)^{-1} \frac{\partial \sigma}{\partial V_1} \right]. \tag{33}
\]

For stability, all of the Floquet multipliers of \(H_5\) must be within the unit circle. So when the period-one orbit loses stability, the stability of the period-two orbit determines whether the period-doubling bifurcation is supercritical or subcritical. For the Hopf bifurcation, two complex conjugate multipliers leave the unit circle away from the real axis. The determination of whether the bifurcation is subcritical or supercritical can be done by calculating the normal form of the bifurcation. An alternate method is to find out whether there exists a periodic response with a period close to \((2\pi)/\text{Im}(\lambda_{\text{Hopf}}))\) (where \(\lambda_{\text{Hopf}}\) is the complex multiplier exiting the unit circle) in the neighborhood of the bifurcation point as the bifurcation parameter is increased. If so then the Hopf bifurcation is supercritical. If not, we can reduce the bifurcation parameter slowly and find out whether there are more than one stable solution by perturbing the period-one solution. If multiple stable solutions coexist, the Hopf bifurcation is subcritical. Of the multiple solutions one is the period-one solution. The others are global solutions.

V. Stability Analysis Using the Linearized Averaged Model

For the averaged model, determination of the stability of the period-one solution can be done using the loop gain \(T_v\) of the voltage loop [30]. The loop gain of the closed-loop buck converter can be determined using

\[
T_v(s) = G_d(s)G_c(s)(FM)(f_s) \tag{34}
\]

where

- \(FM(=1/V_{\text{amp}})\) modulator gain;
- \(f_s = R_z/(R_x + R_y)\) feedback sensor gain;
- \(G_d(s)\) control to the output transfer function;
- \(G_c(s)\) as shown in (35) at the bottom of the page, and \(G_c(s)\) is the controller transfer function. The phase margin of \(T_v\) determines the stability of the closed-loop system.

For the filter, the overall system analysis is done by augmenting the state-space system of the voltage-mode converter with additional states of the filter, which forms its front stage. Analysis of the stability of the augmented system is the same as above once it is modeled with nonlinear maps. For the averaged model, we use the impedance criterion approach originally proposed by Middlebrook [31]. Stability, as per this criterion, demands that the input impedance \((Z_{K3})\) of the closed-loop converter be greater than the output impedance \((Z_{K2})\) of the input filter. For the closed-loop converter, shown in Fig. 1, \(Z_{K1}\) can be shown to be

\[
Z_{K1}(s) = \frac{1 + T_v(s)}{1 + T_v(s) - G_c(s)G_v(s)H(s)(FM)(A)} \tag{36}
\]

where \(Z_{K2}(s), G_c(s), \text{and } G_v(s)\) represent the transfer functions for the open-loop input impedance, the control to the inductor current, and the audio susceptibility, respectively. They are given by (37)–(39), shown at the bottom of the next page. For some designs, if the strictly conservative condition for stability of the integrated system is violated, then an extended analysis
VI. Effect of Parasitics

In the above model, for the closed-loop system, the impact of device nonlinearities and parasitic dynamics was neglected. In conventional commercial converters, the switching frequency is equal to or greater than 100 kHz. At these high frequencies, the parasitics become very important and analysis based on nominal model may not be accurate. If the switching speed of the device (particularly the diode) is fast enough (for example using a Schottky diode), the impact of the device nonlinearity is reduced to some extent as long as the duty ratio of the system is not very small. However, the parasitic parameters vary from board to board, and hence, the parasitic dynamics are hard to control even with a well-designed PCB. To conduct the sensitive experiments reported in this paper, we had to design the board four times and make special arrangements to reduce the common-mode noise.

The impact of these very high-frequency dynamics (commonly called noise by practicing engineers) can be lumped into a stochastic recursion relation [32], [33]

\[ \Psi_{k+1} = f_k(\Psi_k, \omega_k, u) + \Omega_k \chi. \]  

(40)

The quantity \( \Omega_k \) is a random variable controlled by an even distribution of unit width and \( \chi \) is a variable that controls the width (or amplitude) of the noise. Equation (40) is identical to (14a) in the case of a deterministic quantity \( \Omega_k \).

This approach for studying the effect of external noise on the transition to chaos for maps has been done in the past by physicists [32], [33]. However, the assumption of a Gaussian distribution is not always valid in power-electronic systems. This is because the distribution is skewed, primarily due to glitches in the power-converter response, and hence the mean and the median of the distribution are not the same. We consider a more direct approach here. We extract the parameters of the high-frequency model (based on the actual PCB layout) using the INCA software and high-speed design data books [34]. The INCA software is a finite-element analysis package and gives the self inductance associated with a trace and the mutual inductances associated with the coupling between traces [34]. We, however, neglect the mutual inductances because they are smaller than the self inductances by at least three orders of magnitude. As the switching frequency increases to the mega Hertz range, the mutual inductance will become more prominent. Once the parameters have been extracted, we use the SABER software to simulate the higher-order system (Fig. 2). The snubber placed across the diode helps with very high-frequency ringing, with the response dynamics being faster than the switching frequency dynamics by three orders of magnitude. Therefore, we include the snubber in the high-frequency model. The power devices used in the simulation are actual models; they are used to account for the reverse recovery effect and junction capacitances associated with the devices.

VII. Results

We present a bifurcation analysis of the closed-loop buck converter. We compare these results with those obtained by using the averaged model. Then, we present experimental results that we obtained with the buck converter shown in Fig. 3. We use an active load (in resistance mode) to study the influence of load resistance. Moreover, we present the theoretical and experimental results obtained for the integrated filter and converter. Then, we show the impact of the very high-frequency dynamics associated with parasitics and device nonlinearities on the onset of chaos. The system parameters for the closed-loop buck converter, second-order filters, and the high-frequency models are given in Appendix III. The converter we have chosen has a low output voltage and a wide variation in the input voltage. This is a typical scenario in a telecommunication application with a nominal input voltage of 48 V. As such the nominal duty ratio can be very low.

In Fig. 4, we present the frequency-response function obtained with the averaged model for the loop gain of a voltage-mode buck converter. The controller for the closed-loop system has the form \( G_{c}(s) = \frac{1}{(s^2 + \omega_1)(s + \omega_2)(s + \omega_3)} \). The worst phase margin is 20°, and hence the converter is stable according to the small-signal averaged model.

In Fig. 5, we show a bifurcation diagram for this closed-loop system. The load is kept at one ohm, and the input voltage, which is the bifurcation parameter, is varied between 20 to 62 V in increments of 0.1 V. The bifurcation diagram is constructed using the exact method and numerical techniques. The exact method works for \( \omega_1 < 55 \) V. Beyond that the system saturates and hence (15) is indeterminate. To carry on, we use numerical methods. The bifurcation analysis clearly shows a
fast-scale instability, which cannot be predicted with the state-space averaged model. Moreover, prediction of the period-one slow-scale instability with the linearized averaged model is conservative as compared to that obtained with the nonlinear averaged model.

It follows from Fig. 5 that the period-one orbits are stable for all values of the input voltage $v_i$ below 53.8 V. All of the Floquet multipliers are within the unit circle. As $v_i$ increases past 53.8 V, one of the Floquet multipliers exits the unit circle through $-1$, as shown in Fig. 6, indicating a period-doubling or flip bifurcation. To study the stability of the created period-doubled orbits, we calculated the Floquet multipliers based on the second-order map for $v_i = 53.8$ V. The result is $\left[0.1288, 0.8302 + 0.0389i, 0.8302 - 0.0389i, 0.9039, 0.9911\right]$. Because all of these multipliers are inside the unit circle, the created period-two orbits are stable, and hence the period-doubling bifurcation is supercritical. Indeed, we were able to use long-time simulation to calculate period-two orbits beyond $v_i = 53.8$ V, as shown in the bifurcation diagram in Fig. 5.

Fig. 7(a) shows that the duty ratio undergoes a period-doubling bifurcation as $v_i$ increases beyond 53.8 V. Immediately after the period-doubling bifurcation, all of the Floquet multipliers of the second-order map are within the unit circle: three of them are well within the unit circle and two are real and near 1, as shown in Fig. 7(b). In Fig. 7(c), we show the movement of the latter multipliers as $v_i$ increases. They approach each other, collide, and move away from the real axis. It follows from Fig. 7(a) that as $v_i$ approaches $v_i = v_c \approx 54.48$ V, one of the duty ratios approaches zero and at $v_i = v_c$, the error signal $V_e$ hits the ramp at the bottom, as shown in Fig. 7(d). As such, the system saturates, which we have confirmed experimentally. This saturation or pulse dropping initiates the chaos, which is called...
border collision bifurcation by Nusse et al. [35] and Bannerjee et al. [21], [36]. Researchers in other fields have also reported similar nonsmooth bifurcations e.g., C-bifurcations in Filippov systems [37], [38] and grazing bifurcation in impacting systems [39]–[41]. In the saturated region, the second-order map used to derive (27) and (28) becomes invalid. As such, we are unable to plot the movement of the Floquet multipliers any more. For further increase in the input voltage, the response of the system becomes chaotic, which is shown in Fig. 5.

Once chaos is initiated, we resort to time-domain simulation using the switching model. In [42] the concept of impact map has been proposed. However, for the chaotic region, where the switching instant is unknown, the difference in the computation time between the simulation and the approach based on the impact map is negligible. It follows from the bifurcation diagram in Fig. 5 that initially chaos is confined to two small bands. As $u$ is increased, the two chaotic bands increase in size until they collide with the unstable period-one response, resulting in a single large chaotic attractor in a so-called attractor-merging crisis [28].

We now explain, qualitatively, the transition in the response of the closed-loop converter from a period-two orbit to a chaotic attractor in the vicinity of $u = u_c$ by using Filippov’s theory [10]. However, to proceed further, we use (11), (12) and Fig. 1, to describe the dynamics of the states of the closed-loop buck converter as a differential equation with discontinuous right-hand side; that is,

$$\frac{d\Psi(t)}{dt} = A\Psi(t) + BS_1(t) + B_r V_{\text{dref}}$$

$$S_1(t) = \frac{1 + \text{sign}(V_c(t) - V_f(t))}{2}$$

$$= \frac{1 + \text{sign}(V_c(t) - V_f - V_{\text{ramp}} \mod \left(\frac{T}{f}\right))}{2}$$

(41)

where $f = 1/T$, $A = A_1 = A_2$, $B = B_1$, and $B_r = B_{r1} = B_{r2}$. Equation (41) shows that when $V_c(t) = V_f(t)$, then $d\Psi/dt$ is undefined. However, as long as $V_f < V_c(t) < V_f + V_{\text{ramp}}$ at the point of impact with the ramp, the latching action of our controller ensures only a single turn-on and a single turn-off of the power switch in a switching cycle. As such, one can describe the dynamics of the system using a nonlinear discrete map, thereby eliminating the discontinuity. One such map is used to obtain (27) and (28). The general form of this map is

$$\Psi_{k+2} = (\Phi_4 \Phi_3 \Phi_2 \Phi_1)\Psi_k + (\Phi_4 \Phi_3 \Phi_2 \Lambda_1 + \Phi_4 \Phi_3 \Lambda_2 + \Phi_4 \Lambda_3 + \Lambda_4)u$$

$$+ (\Phi_4 \Phi_3 \Phi_2 \Upsilon_1 + \Phi_4 \Phi_3 \Upsilon_2 + \Phi_4 \Upsilon_3 + \Upsilon_4)V_{\text{dref}}$$

Fig. 5. Numerically obtained bifurcation diagram of the closed-loop buck converter. It shows a fast-scale instability.

Fig. 6. One of the period-one Floquet multipliers exits the unit circle through $-1$, indicating a period-doubling bifurcation.
Fig. 7. (a) Variation of the duty ratio and (b) the magnitudes of the Floquet multipliers with the input voltage in the period-two region. (c) Two of the Floquet multipliers in (b) first collide with each other and then move toward the imaginary axis. (d) The error signal hits the ramp at the lowest point (at the beginning of the switching cycle) and the closed-loop system saturates.

$$V_{\alpha_{k+2}} = P_1 \Psi_{k+2}$$
$$0 = \sigma_1(\Psi_{k}, d_{i_k}, u)$$
$$0 = \sigma_2(\Psi_{k}, d_{i_k}, d_{g_k}, u)$$  \hspace{1cm} (42)$$

where $\Phi_s, \Lambda_s, \Upsilon_s$, and $P_1$ are matrices, and the last two scalar equations describe the switching conditions. When $V_e(t) = V_i$ (or $V_i + V_{\text{ramp}}$) at the point of impact with the ramp, (e.g., Fig. 7(d)), the system saturates and hence (42) fails. Using Fig. 7(d), we therefore construct another map as

$$\Psi_{k+2} = (\Phi_3 + \Phi_{s} \Phi_{s} \Phi_{s}) \Psi_{k} + (\Phi_3 + \Phi_{s} \Phi_{s} \Phi_{s} + \Phi_3) \Lambda_s$$
$$V_{de_{k+2}} = P_2 \Psi_{k+2}$$
$$0 = \sigma_1(\Psi_{k}, d_{i_k}, u)$$  \hspace{1cm} (43)$$

where $\Phi_s, \Lambda_s$, and $\Upsilon_s$ are matrices. The nonlinear map (43) is valid at the point given by $V_e(t) = V_i$ and in the vicinity of the fixed point. It is obvious that in the vicinity of saturation, the two maps (given by (42) and (43)) that describe the same system have different forms. Let $J^-$ and $J^+$ be the Jacobian matrices that are obtained by linearizing (42) and (43) in the vicinity of the saturation point. Depending on the values of $J^-$ and $J^+$ in the neighborhood of the saturation point, we can have two different sets of Floquet multipliers ($FM^-$ and $FM^+$). In a smooth system, the transition from $FM^-$ to $FM^+$ is always gradual. However, this is not the case in a nonsmooth system; it has many other other bifurcations than a smooth system [28]. The bifurcation diagram in Fig. 5 shows one such case, where chaos occurs due to saturation immediately after a period-doubling bifurcation. Although the Floquet multipliers are defined before and after the saturation point, they are not defined at this point. The Floquet multipliers at this point are obtained from the set-valued Jacobian matrix $J = [J^-, J^+]$. The viable solutions of this set are obtained by using the concept of convexity [10],
as shown in Appendix IV (for a differential equation with discontinuous right-hand side).

In Fig. 8, we show the experimental results we obtained for the converter in the stable and unstable regions with the input voltage as the bifurcation parameter. In Fig. 8(a), we show a stable period-one response. As $V_i$ is increased to 52.6 V, the response becomes chaotic, as shown in Fig. 8(b), without undergoing a period-doubling bifurcation as predicted by the theory. To explain this discrepancy, we used the high-frequency model, shown in Fig. 2, to account for the impact of the very high-frequency dynamics due to parasitics and device nonlinearities. The high-frequency model is developed using actual device models and parasitic parameters obtained using the actual PCB layout. The parasitic parameters are obtained from the PCB using the finite-analysis package INCA. In Fig. 9, we present the Fourier spectra of the bus voltage for two separate cases: one based on the high-frequency model and the other based on the nominal model, as shown in Fig. 1. These simulation results clearly indicate that the unmodeled uncertainties due to the very high-frequency dynamics cause the earlier onset of chaos.

Interestingly, the chaotic response is stable, and the ripple in the output voltage caused by chaos is tolerable, as seen from the
Fig. 9. Impact of parasitics on the onset of chaos. Fourier spectra of the output voltage based on the nominal model (bottom trace) and on the high-frequency model (top trace). The latter predicts an earlier onset of chaos.

Fig. 10. Experimental result shows the influence of a transient-load change on the chaotic attractor.

experimental waveforms. We also performed a dynamic-load test when the converter is in the chaotic region, and the result is shown in Fig. 10. The load resistance was changed by 150% periodically every 1 ms. The test results show that chaos is stable. Within the chaotic region, we found narrow windows of periodic responses. Two such periodic waveforms are shown in Figs. 11(a) and (b).

Next, we demonstrate the slow-scale instability in the buck converter. The controller for the closed-loop systems has the form $G_c(s) = (\omega_T/s)(s + \omega_{s1})/(s + \omega_{d1})$. The frequency-response function for this case is shown in Fig. 12(a) for input voltages ranging from 20 to 62 V. The load resistance was kept constant at 5 Ω. For the nonlinear model, given the same system parameters, we found that two of the Floquet multipliers $[0.3119, 1.0001 + 0.0899i, 1.0001 - 0.0899i, 0.9533]$ exit the unit circle as complex conjugates at an input voltage of 31 V. Hence, both the averaged and discrete models predict the slow-scale instability.

The experimental result in Fig. 12(d) show that the response of the system suddenly bursts into an oscillation as the input voltage was gradually reduced below the point of instability. Beyond the point of instability, the current is not always in the CCM as seen from the theoretical and experimental results shown in Figs. 12(b) and (c). To analyze the dynamics beyond this point of instability, we had to switch between the CCM and DCM models. The CCM model was augmented with one more additional topology in which both the switch $S_1$ and diode $D_1$ are off. In other words, the system of (5) is modified as

$$X_{k+1} = \Phi_3(t_3)\Phi_2(t_2)\Phi_1(t_1)X_k + \int_{t_{11}}^{T} \Phi_1(\tau)B_1 d\tau + \Phi_3(t_3)\int_{t_{12}}^{T} \Phi_2(\tau)B_2 d\tau + \int_{t_{12}}^{T} \Phi_3(\tau)B_3 d\tau u_k$$

where $\Phi_k(\tau) = e^{\lambda_k \tau}$ for $k = 1,3$ and $t_{1} = t_{11} = d_1T, t_2 = t_{12} - t_{11}, t_3 = T - t_{12}$. The additional state-space matrices $A_3, B_3$, and $C_3$ required for the DCM model are listed in Appendix II. In (4) the duty ratio is calculated as before. The additional timing information needed is the instant at which the inductor goes to zero in the discharging mode.

Next, we present the results obtained for an integrated system, which comprises the buck converter and a second-order filter at its front end. A complete bifurcation analysis for this case can be found in [23]. We consider two separate cases as before. The first is a stable integrated system based on the stability criterion of Middlebrook. The other is a poorly designed filter. The frequency-response plots of $Z_{le}$ and $Z_{le}$ for both cases are shown in Figs. 13(a) and (b). For the second case, in line with the result obtained for the standalone converter, we found that the slow-scale instability can be predicted by both the averaged and nonlinear models. Fig. 14(a) shows that when the input voltage is gradually reduced, the slow-scale instability occurs due to a Hopf bifurcation. Using the normal form of the large-signal averaged model in the vicinity of the bifurcation point, we found that the Hopf bifurcation is subcritical in nature. The averaged model, however, fails to predict the fast-scale instability in the first case, which is predicted by the nonlinear analysis and observed experimentally as shown in Fig. 14(b).

VIII. SUMMARY AND CONCLUSION

Using an exact formulation based on nonlinear maps, we investigate the fast-scale and slow-scale instabilities of a closed-loop converter and predicted the boundaries of these instabilities. For the standalone converter, the fast-scale instability results in a supercritical period-doubling bifurcation
followed by an intermittency of type-I route to chaos. The averaged model does not predict the boundary of the period-one instability correctly. Moreover, it can not account for post-instability dynamics.

We find similar results for an integrated system consisting of a second-order filter and a converter. We investigate two such integrated systems: one is stable as per Middlebrook’s criterion [31] and the other is unstable. We find that this criterion fails to predict the fast-scale instability for the first design. For the second design, which is unstable on the slow scale, this criterion (which is based on linear analysis) does not give the domain of attraction of the period-one orbits near the bifurcation point. Using a nonlinear analysis [23], we find that the domain of attraction of the period-one orbits reduces considerably even before the Hopf bifurcation point due to the simultaneous presence of two stable and one unstable solutions. This reduces the disturbance rejection capability of the closed-loop system in this region. Conventional methods based on small-signal averaged-model techniques do not show this reduction in the domain of attraction.

The impact of parasitics on the onset of chaos is studied using a high-frequency model. The model is developed based on an actual PCB layout and power-device models. We find that the

Fig. 11. Experimental result shows (a) period-4 and (b) period-3 waveform within the chaotic region.
Fig. 12. Slow-scale instability of the closed-loop buck converter. (a) and (b) Linear and nonlinear analyses, which show an unstable response. (c) An experimental result that verifies the result in (b). (d) An experimental result that shows the onset of a sudden slow-scale instability as the input voltage (bifurcation parameter) is changed.

onset of chaos on the fast scale occurs earlier due to parasitics. However, it has negligible impact on the slow-scale instability. This is confirmed experimentally. The boundedness of the response in the chaotic region under transient-load conditions is confirmed experimentally.

The closed-loop converter system should be designed to operate away from the slow-scale instability boundary. However, it can operate close to its fast-scale instability boundary. This will give the system a much higher bandwidth than that obtained using conventional designs. Thus by analyzing the nonlinear dynamics beyond the period-one region, one can improve the performance of the converter and correctly predict the boundaries of the instabilities. To extend the period-one operating regime, one can use bifurcation control.
Fig. 13. Results of an integrated filter-converter system based on small-signal analysis. The frequency-response analyses based on Middlebrook’s criterion predicts (a) stable and (b) unstable system.

APPENDIX I
STATE-SPACE MATRICES FOR THE OPEN-LOOP BUCK CONVERTER

\[
A_1^o = \begin{bmatrix} \frac{-r_L}{L} & -\frac{r_C}{L} & -\frac{R}{L} \\ \frac{R}{L} & \frac{1}{C} & \frac{1}{C} \\ 0 & \frac{1}{C} & \frac{1}{C} \end{bmatrix}, \quad B_1^o = \begin{bmatrix} \frac{r_C}{R} \\ 0 \\ 0 \end{bmatrix}, \quad C_1^o = \begin{bmatrix} \frac{r_C}{R} & \frac{R}{R} & \frac{1}{C} \\ \frac{1}{C} & \frac{1}{C} & \frac{1}{C} \end{bmatrix}
\]

\[
A_2^o = \begin{bmatrix} \frac{-r_L}{L} & -\frac{r_C}{L} & -\frac{R}{L} \\ \frac{R}{L} & \frac{1}{C} & \frac{1}{C} \\ 0 & \frac{1}{C} & \frac{1}{C} \end{bmatrix}, \quad B_2^o = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, \quad C_2^o = \begin{bmatrix} \frac{r_C}{R} & \frac{R}{R} & \frac{1}{C} \\ \frac{1}{C} & \frac{1}{C} & \frac{1}{C} \end{bmatrix}
\]

\[
A_3^o = \begin{bmatrix} 0 \\ 0 \\ -\frac{1}{L} \end{bmatrix}, \quad B_3^o = \begin{bmatrix} 0 \\ 0 \\ 0 \end{bmatrix}, \quad C_3^o = \begin{bmatrix} \frac{r_C}{R} & \frac{R}{R} & \frac{1}{C} \\ \frac{1}{C} & \frac{1}{C} & \frac{1}{C} \end{bmatrix}
\]

(for DCM only)

APPENDIX II
STATE-SPACE MATRICES FOR THE CLOSED-LOOP BUCK CONVERTER

\[
A_1 = \begin{bmatrix} A_1^o & 0 \\ SC_1 & A_c \end{bmatrix}, \quad B_1 = \begin{bmatrix} B_1^o \\ 0 \end{bmatrix}, \quad B_{r1} = \begin{bmatrix} 0 \\ B_{rc} \end{bmatrix}
\]

\[
C_1 = \begin{bmatrix} C_1^o \\ 0 \end{bmatrix}, \quad H_1 = [H_c 0]
\]

\[
A_2 = \begin{bmatrix} A_2^o & 0 \\ f_s C_2 & A_c \end{bmatrix}, \quad B_2 = \begin{bmatrix} B_2^o \\ 0 \end{bmatrix}, \quad B_{r2} = \begin{bmatrix} 0 \\ B_{rc} \end{bmatrix}
\]

\[
C_2 = \begin{bmatrix} C_2^o \\ 0 \end{bmatrix}, \quad H_2 = [H_c 0]
\]

\[
A_3 = \begin{bmatrix} A_3^o & 0 \\ f_s C_3 & A_c \end{bmatrix}, \quad B_3 = \begin{bmatrix} B_3^o \\ 0 \end{bmatrix}, \quad B_{r3} = \begin{bmatrix} 0 \\ B_{rc} \end{bmatrix}
\]

\[
C_3 = \begin{bmatrix} C_3^o \\ 0 \end{bmatrix}, \quad H_3 = [H_c 0]
\]

(for DCM only)

where \( f_s \) is the feedback sensor gain.

APPENDIX III
PARAMETERS OF THE CLOSED-LOOP SYSTEM

Nominal model parameters

\[
L = 50 \mu \text{H}, \quad r_L = 21 \text{ m}\Omega, \quad r_C = 21 \text{ m}\Omega,
\]

\[
C_f = 4400 \mu \text{F}, \quad R = 1\Omega-\text{SLO}.
\]

High-frequency model parameters

\[
r_L = 21 \text{ m}\Omega, \quad r_C = 42 \text{ m}\Omega, \quad L = 50 \mu \text{H},
\]

\[
C_4 = 2200 \mu \text{F}, \quad C_5 = 2200 \mu \text{F}, \quad C_1 = 30 \text{ pF},
\]

\[
L_2 = 25 \text{ n}\text{H}, \quad R_2 = 150 \text{ m}\Omega, \quad L_3 = 5 \text{ n}\Omega,
\]

\[
R_3 = 25 \text{ m}\Omega, \quad C_6 = 100 \text{ nF}, \quad C_3 = 100 \text{ nF},
\]

\[
R_5 = 50 \text{ m}\Omega, \quad L_5 = 15 \text{ n}\Omega.
\]

Filter parameters

\[
L_f = 200 \mu \text{H}, \quad R_{f1} = 100 \text{ m}\Omega, \quad R_{f2} = 21 \text{ m}\Omega,
\]

\[
C_f = 440 \mu \text{F}.
\]

Fig. 14. (a) Nonlinear analysis shows a slow-scale instability for the second filter-converter system. (b) Experimental result that shows a fast-scale instability for the first filter-converter system.
APPENDIX IV

FILIPPOV’S SOLUTION FOR A DISCONTINUOUS DIFFERENTIAL EQUATION

Let us consider the following vector differential equation:

\[ \dot{y} = f(y, t, u(y)) = h(y, t) \]  \hfill (A1)

where \( h : \mathbb{R} \times \mathbb{R}^n \to \mathbb{R}^n \) is measurable and essentially locally bounded. A vector function \( y(t) \), defined on the interval \((t_1, t_2)\), is a Filippov solution [10] of (A1) if it is absolutely continuous and, for almost all \( t \in (t_1, t_2) \) and for arbitrary \( \delta > 0 \), the vector \( \frac{dy(t)}{dt} \) belongs to the smallest convex closed set of an \( n \)-dimensional space containing all of the values of the vector function \( h(t, y(t)) \); where \( y(t) \) ranges over the entire \( \delta \) neighborhood of the point \( y(t) \) in the space \( y \) (with \( t \) fixed) except for a set of measure \( \mu_\mathcal{N} = 0 \); that is

\[ \frac{dy(t)}{dt} \in K[h](y, t) \]  \hfill (A2)

where \( K[h](\cdot) \) is called the Filippov’s differential inclusion and is defined as

\[ K[h](t, y) = \bigcap_{\varepsilon > 0} \bigcap_{\mu \in \mathcal{N}, \varepsilon > 0} \overline{\cap}_\varepsilon h(B(y, \delta) - \mathcal{N}). \]  \hfill (A3)

In differential inclusion (A2), \( \overline{\cap}_\varepsilon \) denotes the convex hull of a set, \( \mathcal{N} \) represents a set of zero Lebesgue measure, \( \mu N \) and \( B \) is a ball of radius \( \delta \) centered at \( y \). The content of Filippov’s solution is that the tangent vector to a solution at a time \( t \), where it exists, must lie in the convex closure of the limiting values of the vector field in progressively smaller neighborhoods around the solution evaluated at time \( t \).

Let us now consider a single switching surface \( H \), which is a smooth surface (manifold) separating the space into regions \( H^+ \) and \( H^- \). Suppose that \( H \) is regular so that it can be divided by a smooth real-valued function \( \sigma(y) \) (i.e., \( H = \{ y : \sigma(y) = 0 = 0 \} \) and suppose that \( h(t, y) \) is bounded and, for any fixed \( t \), its limiting values \( h^+(y, t) \) and \( h^-(y, t) \) exist when \( H \) is approached from \( H^+ \) and \( H^- \). Let \( h^+_0(y, t) \) and \( h^-_0(y, t) \) be the projections of \( h^+(y, t) \) and \( h^-(y, t) \) on the normal \( \nabla \sigma \) to the surface \( H \) directed toward \( H^+ \) and \( H^- \). Then, for an absolutely continuous \( y \in H \) satisfying \( h^+_0(y, t) \leq 0, h^-_0(y, t) \geq 0 \), and \( h^-_0(y, t) - h^+_0(y, t) > 0 \), the trajectories pointing toward \( H \) are solutions of (A1) according to the differential inclusion (A2) if and only if

\[ \frac{dy}{dt} = \beta(t)h^+(y, t) + (1 - \beta(t))h^-(y, t) \]  \hfill (A4)

where

\[ \beta(t) = \frac{h^+_0(y, t)}{h^-_0(y, t) - h^+_0(y, t)}. \]  \hfill (A5)

We note that the right-hand side of (A4) is orthogonal to \( \nabla \sigma \) and hence the solution remains on the surface \( H \).
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