ECE 465 — Digital Systems
Spring 2015, T/Th 3:30-4:45 pm, 230 SES

Teaching Staff
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Important: Description and Philosophy
This course will discuss techniques and theories of the art and science of designing and optimizing semi-complex to complex combinational and sequential circuits, and for testing the correctness of implemented circuits (whatever the implementation platform be: PCBs with discrete components or IC chips). The course will stress analytical and critical thinking, and creativity as opposed to rote or recipe-based learning (the latter skills will not be helpful at all in this course). These are the skills and temperament that are highly desired in the current technology and innovation-hungry industry. The types of designs, problem-solving and formal proofs that will be be asked many times in homeworks, projects and exams will require good analytical and in-depth thinking.

To recap, the course will teach and require:

1. Creativity: In-depth thinking, creativity and ingenuity in solving semi-complex problems.

2. Analytical Rigor: Mathematical astuteness and in-depth thinking to perform analysis of circuits (e.g., hardware cost and delay analysis) and design techniques (e.g. optimality or correctness proofs of techniques).

3. Motivation to excel.

4. Integrity.

It is hoped that those who take this course buy in enthusiastically to the aforementioned learning philosophy, and are willing to execute it.

Course Material

Lecture Notes: Almost all will be put online on the course web page: www.ece.uic.edu/~dutt/courses/ece465/ece465.html
Homeworks, Projects and Quizes

Homework: About five homeworks (HWs) will be assigned. Homeworks are to be submitted ONLY in class on the due date. Late HWs will not be accepted.

Projects: There will be 1-2 projects (depending on resources available) using the Quartus software. Projects are to be submitted as electronic copies of three main items (some other items may be requested based on the project): (a) the project report, (b) the design or code, as the case may be, and (c) the simulation results, by midnight on the due date. All items are to be e-mailed to the TA, and only the project report is to be e-mailed to me.

Quizes: There will be 1 quiz that will mainly be given to test your preparation of background material that are extremely important for understanding the course material. You will be told about the background material needed for the course material from time to time (a list is also given later in this syllabus) and will be informed of the corresponding quiz.

Examination Schedule

All exams will be closed everything (books, notes, etc.). No make-up exams will be given except in extreme circumstances like a serious health issue that is documented and verifiable. You must contact the instructor prior to the exam in order to arrange for a make-up. It is possible that the exams will be made take-home requiring electronic submission using text-processing and figure-drawing s/w such as MS Word and Powerpoint (with final conversion to a pdf document).

Midterm Exam: In the 8’th or 9’th week of the semester.

Final Exam: Either in the weekend after last class (assuming a take-home exam) or during finals week, but not necessarily as scheduled by the official system if it is a take-home exam.

Grade Distribution–tentative

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Honor Code

The following Honor Code policy shall be in effect in this course:

- Not to seek unfair advantage over other students, including, but not limited to: (a) giving or receiving unauthorized aid (i.e., outside of your official project work team) during completion of academic requirements (this includes lab work, homeworks and exams); (b) obtaining past semesters’ project and homework solutions and creating your project/homework from them.

For your information, we have electronic copies of past student projects and can verify if your project’s design/code has any similarity with that of past projects (even if you change entity names and or some
parts of a past project). Past students have also been told not to give anyone their projects or homework solutions, and to report if anyone asks for them. So, please do not even think of asking for these, if you value honesty, fairness, and your status as a student in good standing at UIC.

- To represent fact and self truthfully at all times.

- Not to pass on your project work or homework solutions to anyone else ever, either in this semester, or at any future time including after you graduate.

Violations of the Honor Code are just causes for discipline under the University of Illinois at Chicago Student Disciplinary Procedure, and all allegations of Honor Code violations shall be handled pursuant to that Procedure.

**Course Outline**

**Note:** The sections of the text listed below in square brackets for the various topics may not always cover all subtopics discussed in class or may cover more subtopics than discussed in class, as the book will not be followed 100%. Handouts will be given on some occasions. But mainly it is important to attend all lectures in order to learn all the material and do well in the course. Lecture #s corresponds to 1 hr 15 min lectures.

1. **Introduction (1.5 lecture)** (Sec. 0.2).

2. **Review of Basics (1.5 lectures)**
   Boolean algebra, fundamental definitions (implicants, prime implicants, implicates, prime implicates, etc.), minimization using Boolean algebra, minimization using K-maps, hazards in combinational circuits and their solutions, synthesis using NAND/NOR gates [1.1, 1.3-1.4, 2.1-2.2, 2.5, 3.1-3.8]

3. **Advanced Two-level Combinational Circuit Minimization (4 lectures)**
   Quine-McCluskey method (single and multiple functions), Petrick’s algorithm [3.9-3.10]

4. **Component-Based Combinational Circuit Synthesis (MUXes, PLAs, PALs) (4 lectures)** [4.2, 4.4, 5.1-5.5]

5. **High-Level Design Approaches including Divide-&-Conquer (4 lectures)**

6. **Synchronous Sequential Circuits (9 lectures)**
   finite state machine (FSM) synthesis (Moore, Mealy), synthesis of synchronous sequential circuits, clocking methods, state minimization and state assignment techniques, one-hot design style [8.1-8.4, 9.1-9.4]

7. Either:
   a) **Testing of Combinational and Sequential Circuits (3 lectures)** [12.1-12.4]
   OR
   b) **Asynchronous Sequential Circuits (3 lectures)**
   Synthesis (pulse & fundamental modes), races and hazards [10.1-10.6]
Background Review Material

(a) Logic Design Background: Please carefully review the following background material from ECE 265 for you to understand the lectures in ECE 465 and do well in the quizzes. The secs. in text where these material are available are listed in square brackets. The week # given is the week in which these material have to be reviewed by you.

1. **Introduction:** [0.1-0.2] – week 1
2. **Number Systems and Codes:** [1.1-1.2] – week 1
3. **Boolean Algebra:** [2.1-2.2] – week 1
4. **K-maps:** [3.1-3.7] – weeks 1 and 2
5. **Logic gates, synthesis of logic circuits using NAND/NOR gates:** [2.3-2.4.1, 2.5-2.6] – week 2
   Topics in items 4 and 5 also available at: http://www.ece.uic.edu/~dutt/courses/ece465/lect-notes/lect2-new-465.ppt
6. **Latches and FFs:** [6.1-6.4.2 except the discussion on 74LS75 and 74116 D-latches] – week 7

(b) Discrete Math Background: While CS 201 is not a formal pre-requisite for this course, nevertheless the following discrete math background is needed for this course (and is standard requirement at all strong schools). While these topics will be introduced and quickly covered as and when required, it will be much better for you to review these topics throughly from either the CS 201 textbook or by googling these topics (wikipedia and math.com are good sources).

1. **Sets and set theory** (mild requirement; only simple and intuitive set theory concepts like disjointness, intersection, union will be needed) – week 2
2. **Sequences and series** (arithmetic and geometric series in particular) – week 2
3. **Complexity order notations** (big Oh, big Theta, and big Omega notations) – week 3
4. **Elementary probability theory** (especially the average of a metric with a given probability distribution) – week 3
5. **Proof techniques** (e.g., proof by induction, proof by contradiction) – week 4